

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,148,541 B2
APPLICATION NO. : 10/780067
DATED : December 12, 2006
INVENTOR(S) : Park et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12,

Line 12 should read -- a gate insulating layer that are formed on upper sidewalls --

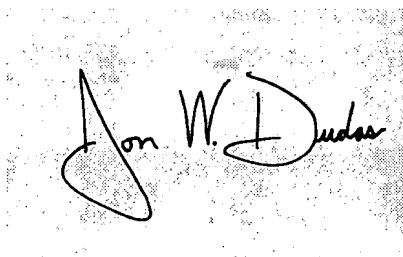
Line 23 should read -- is uniform, and a lower width of the fin coated with the --

Column 14,

Line 9 should read -- source and drain regions formed in the pillar at both sides --

Signed and Sealed this

Tenth Day of April, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a light gray, textured rectangular background.

JON W. DUDAS
Director of the United States Patent and Trademark Office